### Power Management in Linux\* - State of The Art

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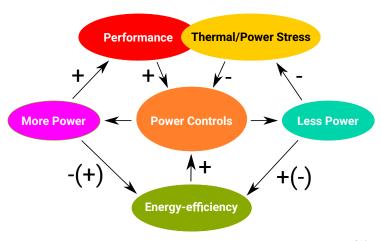
## What Power Management Is About







# General Overview Of Power Management





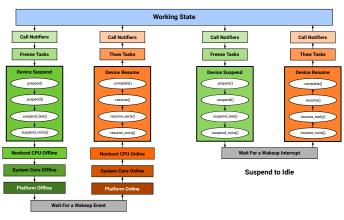


### Two Different Ways To Get There





## System Suspend



Platform-based Suspend



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# System Suspend/Hibernation Interface In sysfs

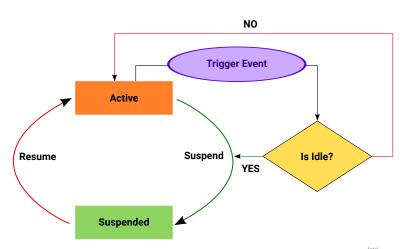
/sys/power/state
freeze mem disk
/sys/power/mem\_sleep
[s2idle] deep
/sys/devices/.../power/wakeup
enabled
disabled







### Overview Of PM-runtime





## PM-runtime Control Through sysfs

/sys/devices/.../power/control auto on

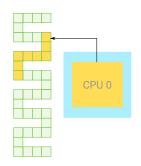
/sys/devices/.../power/autosuspend\_delay\_ms

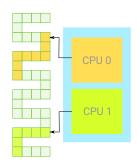


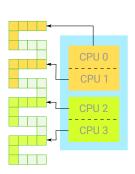




## CPUs Are Logical Entities









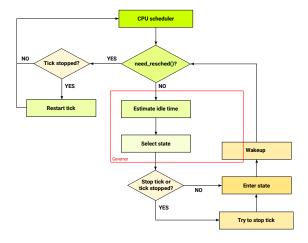
### CPUs: Busy Vs Idle







# CPU Idle Loop (Linux\* 4.17 And Later)





\* Other names and brands may be claimed as the property of others

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## CPU Idle Time Management Control

#### Kernel command line

idle=<mark>poll</mark> halt nomwait

cpuidle.off=1

intel\_idle.max\_cstate=0 1 ... 9

processor.max\_cstate= 1 ... 9 0

### Special device

/dev/cpu dma latency

#### sysfs

/sys/devices/system/cpu/cpuidle/ current\_driver:intel\_ldle current\_governor\_ro:menu

/sys/devices/system/cpu/cpu<nr>/cpuidle/state<nr>/
desc: MWAIT 0x00

disable: 0 latency: 2 name: C1 power: 0 residency: 2 time: 438396006 usage: 4637114

/sys/devices/system/cpu/cpu<nr>/power/pm\_qos\_resume\_latency\_us



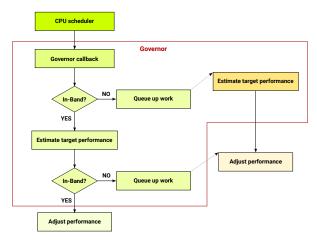


### CPUs: Performance And Utilization





## Overview Of CPU Performance Scaling







### In-Band Scaling Governors

#### intel\_pstate powersave (Active Mode) schedutil cpufreq\_update\_util() cpufreq\_update\_util() Adjust performance (Driver) Governor callback Governor callback NO Fast switching Estimate target performance Compute frequency Queue up work supported? YES Adjust performance Adjust performance (Driver)





## **CPU Performance Scaling Control**

#### sysfs

```
/svs/devices/svstem/cpu/cpu<nr>/cpufreg/
     affected_cpus: 0
     cpuinfo max freq: 4300000
     cpuinfo_min_freq: 800000
     cpuinfo_transition_latency: 0
     energy performance available preferences; default performance balance performance balance power power
     energy_performance_preference: balance_performance
     related cous: 0
     scaling_available_governors: performance powersave
     scaling_cur_freq: 3039632
     scaling driver; intel pstate
     scaling governor : powersave
     scaling_max_freq: 4300000
     scaling min freg: 800000
/sys/devices/system/cpu/intel_pstate/
     max perf pct: 100
     min perf pct: 18
     no_turbo: 0
     num pstates: 36
     status: active
     turbo_pct: 20
```

#### Kernel command line

cpufrea.off=1

intel\_pstate=active disable force hwp\_only no\_hwp passive per\_cpu\_perf\_limits



support\_acpi\_ppc

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# PCI Express Active State Power Management (ASPM)





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### PCIe ASPM Control

### Module parameter

```
pcie_aspm.policy=default
performance
powersave
powersupersave
```

### sysfs

```
/sys/devices/pci0000:00/.../link/
clkpm:1
l0s_aspm:1
l1_1_aspm:1
l1_1_pcipm:1
l1_2_aspm:1
l1_2_pcipm:1
l1_aspm:1
```

#### Kernel command line

```
pcie_aspm= off
force
```







# Intel Performance and Energy Bias Hint (EPB)





### Intel EPB Control Through sysfs

#### /sys/devices/system/cpu/cpu<nr>/power/energy\_perf\_bias

0...15 performance

balance-performance

normal

balance-power power







### Is Energy-efficiency Always At Odds With Performance?







### Connection Between Energy-efficiency And Performance

### For individual hardware components

Performance depends on the capacity and latency.

### Without power budget sharing

Better energy-efficiency means more latency and/or less capacity.

#### However

Improving energy-efficiency of one component may change the distribution of power.





### PM Features May Depend On One Another





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### Questions? Comments? Concerns?





### References



The Linux kernel user's and administrator's guide, *Power Management* (https://www.kernel.org/doc/html/latest/admin-guide/pm/index.html).



Rafael J. Wysocki, Energy-efficiency and Linux

 $(\verb|https://static.sched.com/hosted_files/osseu19/d1/energy-efficiency_and\_Linux.pdf)|.$ 



Rafael J. Wysocki, Advances in CPU Idle Time Management (http://events19.linuxfoundation.org/wp-content/uploads/2017/11/Advances-in-CPU-Idle-Time-Management-Rafael-Wysocki-Intel.pdf).



Rafael J. Wysocki, *Power Management Challenges in Linux* (https: //www.linuxplumbersconf.org/2017/ocw//system/presentations/4652/original/linux\_pm\_challenges.pdf).



Rafael J. Wysocki, Advances in CPU Performance Scaling (http://schd.ws/hosted\_files/ossna2017/39/advances\_in\_cpu\_perf\_scaling.pdf).



Rafael J. Wysocki, PM Infrastructure in the Linux Kernel — Current Status and Future (https://events.linuxfoundation.org/sites/events/files/slides/kernel\_PM\_infra\_0.pdf).



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